

Interference Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(vga or (variable adj gain adj amplifier)) and (pa or (power adj amplifier)) and transistor and (differential adj pair) and (collector adj circuit) and (bias adj current adj source) and bipolar. clm.	US-PGPUB	OR	ON	2005/08/05 09:35

Search
Strategy

APPLICATION # 10816516

BRS	L11	765	(vga or (variable adj gain adj amplifier)) same (pa or (power adj amplifier))	US-PGPUB; USPAT; EPO; JPO; DERWENT
BRS	L12	157	330/\$.ccls. and (vga or (variable adj gain adj amplifier)) same (pa or (power adj amplifier))	US-PGPUB; USPAT; EPO; JPO; DERWENT
BRS	L13	10	12 and (differential adj pair)	US-PGPUB; USPAT; EPO; JPO; DERWENT
BRS	L14	43	12 and bipolar	US-PGPUB; USPAT; EPO; JPO; DERWENT
BRS	L15	2	12 and (bias adj current adj source)	US-PGPUB; USPAT; EPO; JPO; DERWENT
BRS	L16	31	2 and (bipolar adj transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT
BRS	L17	18	2 and (differential adj pair)	US-PGPUB; USPAT; EPO; JPO; DERWENT
BRS	L18	31	2 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT
BRS	L19	2702	(vga or (variable adj gain adj amplifier)) and (pa or (power adj amplifier))	US-PGPUB; USPAT; EPO; JPO; DERWENT